

WHAT IS CLAIMED IS:

1. An apparatus for dividing a bank in a flash memory, the flash memory having a first bank and a second bank that share an input/output line, comprising;

5 a first page buffer connected to a bit line of the first bank, and buffering a data that will be transmitted to/from the first bank;

a second page buffer connected to a bit line of the second bank, and buffering a data that will be transmitted to/from the second bank;

10 a first page buffer select mean connected between the first page buffer and a first node, and controlling a transmission of the data that will be transmitted to/from the first page buffer;

a second page buffer select mean connected between the second page buffer and the first node, and controlling transmission of the data that will be transmitted to/from the second page buffer; and

15 a switching mean connected between the first node and the input/output line, and controlling transmission of the data between the first and second page buffer select mean and the input/output line.

2. The apparatus as claimed in claim 1, wherein the first page buffer select mean comprises a first transistor connected between the first page buffer and the first node, and driven by an external first control signal.

3. The apparatus as claimed in claim 1, wherein the second page buffer select mean comprises a second transistor connected between the second page buffer and a second node, and driven by an external second control signal.

5       4. The apparatus as claimed in claim 1, wherein the switching mean comprises a third transistor connected between the first node and the input/output line, and driven by an external third control signal.